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Lee W. Henderson, Ph.D.

April 8, 2009

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: Title: DYNAMIC AND DIFFERENTIAL CMOS LOGIC WITH SIGNAL-
INDEPENDENT POWER CONSUMPTION TO WITHSTAND
DIFFERENTIAL POWER ANALYSIS
Letters Patent No. 7,417,468
Issued: August 26, 2008
Our Reference No.: UCLARF.003NP

Dear Sir:

Enclosed for filing is a Certificate of Correction in connection with the above-identified patent.

As the errors cited in the Certificate of Correction were incurred through the fault of the Patent Office, no fee is believed to be required. However, please charge our Deposit Account No. 11-1410 for any fees that may be incurred with this request.

Respectfully submitted,

Knobbe, Martens, Olson & Bear, LLP



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6949646
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310-551-3450

Riverside
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Seattle
206-405-2000

Washington, DC
202-640-6400

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,417,468
APPLICATION NO. : 10/565,551
ISSUE DATE : August 26, 2008
INVENTOR(S) : Verbauwhede et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 5 at approximately line 17, change "VT" to --V_t--.

In column 7 at line 60, change "DPUTN" to --DPUN--.

In column 8 at line 8, after "event" insert --,--.

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DOCKET NO. UCLARF.003NP